PCT

WORLD INTELLECTUAL PROPERTY ORGANIZATION
International Bureau

INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 6 : G02B 26/00

A1

(11) International Publication Number: WO 97/17628

(43) International Publication Date: 15 May 1997 (15.05.97)

(21) International Application Number: PCT/US96/17731

(22) International Filing Date: 6 November 1996 (06.11.96)

(30) Priority Data:
08/554,630 6 November 1995 (06.11.95) US

(71) Applicant: ETALON, INC. [US/US]; 33 Hanson Street #2, Boston, MA 02118 (US).

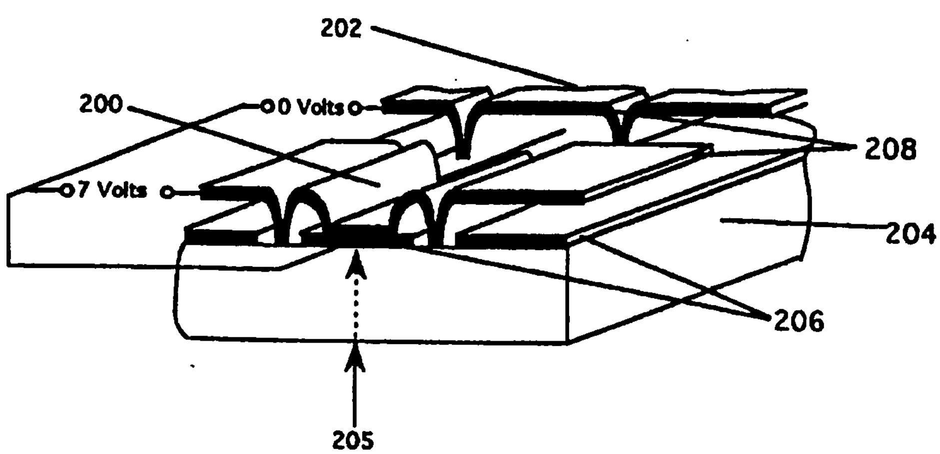
(72) Inventor: MILES, Mark, W.; 33 Hanson Street #2, Boston, MA 02118 (US).

(74) Agent: FEIGENBAUM, David, L.; Fish & Richardson P.C., 225 Franklin Street, Boston, MA 02110-2804 (US).

(81) Designated States: JP, European patent (AT, BE, CH, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).

Published
With international search report.

(54) Title: INTERFEROMETRIC MODULATION



(57) Abstract

an interferometric modulator cavity (200, 202) has a reflector (208) and an induced absorber (206). A direct view reflective flat panel display may include an array of the modulators. Adjacent spacers of different thicknesses are fabricated on a substrate (204) by a lift-off technique used to pattern the spacers which are deposited separately, each deposition providing a different thickness of spacer. Alternately, a patterned photoresist may be used to allow for an etching process to selectively etch back the thickness of a spacer which was deposited in a single deposition. A full-color static graphical image may be formed of combined patterns of interferometric modulator cavities (200, 202). Each cavity includes a reflector (208), and an induced absorber (206), the induced absorber (206) including a spacer having a thickness that defines a color associated with the cavity.

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AM	Armenia	GB	United Kingdom	MW	Malawi
AT	Austria	GE	Georgia	MX	Mexico
AU	Australia	GN	Guinea	NE	Niger
BB	Barbados	GR	Greece	NL	Netherlands
BE	Belgium	HU	Hungary	NO	Norway
BF	Burkina Faso	IE	Ireland	NZ	New Zealand
BG	Bulgaria	IT	Italy	PL	Poland
BJ	Benin	JP	Japan	PT	Portugal
BR	Brazil	KE	Kenya	RO	Romania
BY	Belarus	KG	Kyrgystan	RU	Russian Federation
CA	Canada	KP	Democratic People's Republic of Korea	SD	Sudan
CF	Central African Republic			SE	Sweden
CG	Congo	KR	Republic of Korea	SG	Singapore
CH	Switzerland	KZ	Kazakhstan	SI	Slovenia
CI	Côte d'Ivoire	LI	Liechtenstein	SK	Slovakia
CM	Cameroon	LK	Sri Lanka	SN	Senegal
CN	China	LR	Liberia	SZ	Swaziland
CS	Czechoslovakia	LT	Lithuania	TD	Chad
CZ	Czech Republic	LU	Luxembourg	TG	Togo
DE	Germany	LV	Latvia	TJ	Tajikistan
DK	Denmark	MC	Monaco	TT	Trinidad and Tobago
EE	Estonia	MD	Republic of Moldova	UA	Ukraine
ES	Spain	MG	Madagascar	UG	Uganda
FI	Finland	ML	Mali	US	United States of America
FR	France	MN	Mongolia	UZ	Uzbekistan
GA	Gabon	MR	Mauritania	VN	Viet Nam

INTERFEROMETRIC MODULATION

Background

This is a continuation in part of United States Patent Application Serial Number 08/238,750, filed May 5, 1994, and incorporated by reference.

This invention relates to visible spectrum (including ultra-violet and infrared) modulator arrays.

The parent application describes two kinds of structures whose impedance, the reciprocal of admittance, can be actively modified so that they can modulate light. One scheme is a deformable cavity whose optical properties can be altered by electrostatic deformation of one of the cavity walls. The composition and thickness of these walls, which consist of layers of dielectric, semiconductor, or metallic films, allows for a variety of modulator designs exhibiting different optical responses to applied voltages.

One such design includes a filter described as a hybrid filter which has a narrow bandpass filter and an induced absorber. When the wall associated with the hybrid filter is brought into contact with a reflector, incident light of a certain range is absorbed. This occurs because the induced absorber matches the impedance of the reflector to that of the incident medium for the range of frequencies passed by the narrow-band filter.

Summary

This invention eliminates the need for the narrow-band filter and provides a much broader absorption range.

The invention modulates light by electrostatically varying the spacing of a cavity comprising two walls, one of which is a reflector and the other is the induced absorber. The cavity is fabricated on an optically smooth substrate, i.e., sufficiently smooth to allow for the manifestation of interference effects.

Thus, in general, in one aspect the invention features an interferometric modulator cavity having a reflector and an induced absorber.

Implementations of the invention may include one or more of the following features. The reflector may include films of metal, dielectric, semiconductor, or a combination of them. The induced absorber may include a sandwich of an absorber between two matching layers. One of the matching layers may reside at the boundary of the absorber with an incident medium and the other matching layer may reside at the boundary of the absorber with the reflector. At least one of the matching layers may include a film of metal. At least one of the matching layers may include a dielectric film, or a semiconducting film, or a combination of at least two of a metal film, a dielectric film, and a semiconducting film. The absorber may include a high loss film such as a metal, or a high loss film such as a semiconductor, or a combination of a metal and semiconducting film. There may also be a substrate which includes a transparent incident medium. The induced absorber and/or the reflector may reside on the substrate. The substrate may be transparent, in which case it could also act as the incident medium, or opaque. The spacer may be air or some other pliant medium (e.g., liquid or plastic) which would allow the thickness of the gap to be altered.

In general, in another aspect, the invention features a direct view reflective flat panel display comprising an array of interferometric modulators.

Implementations of the invention may include one or more of the following. The array may include sets of the interferometric modulators, the respective sets being arranged to switch between different pairs of reflective states. The array may include a single set of interferometric modulators, the set being arranged to be

driven in an analog fashion to reflect any particular color. The brightness of each of the modulators is controlled by pulse width modulation, or by spatial dithering, or by a combination of the two. The array may be sealed by a backplane. The backplane may include a monolithic element. The backplane may be attached. The backplane may support electrodes which modify the electromechanical response of the pixels. Each of the modulators may be actuated by electrostatic forces or by piezoelectric forces or by magnetic forces. The display may be used in a projection system. An optical compensation mechanism may be used to mitigate or eliminate a shift in color with respect to viewing angle or to provide supplemental frontlighting or to mitigate or eliminate a shift in color with respect to viewing angle. The substrate may be an integrated circuit.

In general, in another aspect, the invention features a process for fabricating adjacent spacers of different thicknesses on a substrate in which a lift-off technique is used to pattern the spacers which are deposited separately, each deposition providing a different thickness of spacer. Or a patterned photoresist may be used to allow for an etching process to selectively etch back the thickness of a spacer which was deposited in a single deposition.

In general, in another aspect, the invention features a full-color static graphical image comprising an array of interferometric modulator cavities. Each cavity includes a reflector, and an induced absorber, the induced absorber including a spacer having a thickness that defines a color associated with the cavity.

In general, in another aspect, the invention features a full-color static graphical image comprising separate patterns of spacers or interferometric modulator cavities with spacers, in each pattern the spacer having

a thickness which defines a color associated with the pattern which when all patterns are combined produces the image.

Among the advantages of the invention may be one or more of the following. High quality full-color flat panel displays may be made possible by using pixels based on these new cavities. By fabricating a pixel which switches between two colors (for example red and black) then a flat-panel display may be fabricated by combining three sets of these pixels designed to switch between red and black, green and black, and blue and black respectively. The inherent color precludes the need for color filter arrays which are typically required for color LCDs. Additionally reflective displays, which are displays which use ambient light instead of backlighting, are particularly susceptible to pixel inefficiencies. Because the cavities of the invention can use greater than 90% of the incident light, they are excellent candidates for this application. These structures, when driven electrostatically, also exhibit a micro-electromechanical hysteresis which can be exploited to eliminate the need for transistors.

Other advantages and features of the invention will become apparent from the following description and from the claims.

<u>Description</u>

Fig. 1 is a diagram of layers a modulator.

Fig. 2 is a perspective view of cavities in a device.

Fig. 3 is a diagram is a side view of a pixel device.

Fig. 4 is a graph of the optical response for a cavity which appears black.

Fig. 5 is a graph of the optical response for a cavity which appears blue.

Fig. 6 is a graph of the optical response for a cavity which appears green.

Fig. 7 is a graph of the optical response for a cavity which appears red.

Fig. 8 is a graph of the optical response for a cavity which appears white.

Fig. 9 is a perspective view of a fragment of a reflective flat panel display.

Figs. 10a, 10b, 10c, 10d are perspective views of different spacers during fabrication.

Figs. 11a, 11b, 11c, 11d are also perspective views of different spacers during fabrication.

Figs. 12a, 12b, 12c, 12d are top views of a static graphic image.

Any thin film, medium, or substrate (which can be considered a thick film) can be defined in terms of a characteristic optical admittance. By considering only the reflectance, the operation of a thin film can be studied by treating it as an admittance transformer. That is, a thin film or combination of thin films (the transformer) can alter the characteristic admittance of another thin film or substrate (the transformed film) upon which it is deposited. In this fashion a normally reflective film or substrate may have its characteristic admittance altered (i.e. transformed) in such a way that its reflectivity is enhanced and/or degraded by the deposition of, or contact with, a transformer. In general there is always reflection at the interface between any combination of films, mediums, or substrates. The closer the admittances of the two, the lower the reflectance at the interface, to the point where the reflectance is zero when the admittances are matched.

Referring to Fig. 1, reflector 100 (the transformed film) is separated from induced absorber 105

(the transformer), comprising films 104, 106, and 108, by variable thickness spacer 102. Incident medium 110 bounds the other side of induced absorber 105. Each of these thin films is micromachined in a fashion described in the parent patent application. Induced absorber 105 performs two functions. The first is to match the admittances of reflector 100 and incident medium 110. This is accomplished via matching layer 108, which is used to transform the admittance of absorber 106 to that of the incident medium 110, and via matching layer 104, which is used to transform the admittance of reflector 100 to that of absorber 106. The second function is the absorption of light. This is accomplished using absorber 106, which performs the function of attenuating light which is incident upon it through the medium, as well as light which is incident upon it from the reflector.

The ability to alter the thickness T of spacer 102 allows the optical characteristics of the entire structure to be modified. Referring to Fig. 2, pixel 200 is shown in the driven state and pixel 202 in the undriven state. In this case induced absorber 206 (the transformer) resides on substrate 204 and reflector 208 (the transformed film) is a self-supporting structure. Application of a voltage causes reflector 208 to come into contact or close proximity with induced absorber 206. Proper selection of materials and thicknesses will result in a complete transformation of the admittance of reflector 208 to that of substrate 204. Consequently, a range of frequencies of light 205, which is incident through substrate 204, will be significantly absorbed by the pixel. With no voltage applied, reflector 208 returns to its normal structural state which changes the relative admittances of the reflector and the substrate. In this state (pixel 202) the cavity behaves more like a

resonant reflector, strongly reflecting certain frequencies while strongly absorbing others.

Proper selection of materials thus allows for the fabrication of pixels which can switch from reflecting any color (or combination of colors) to absorbing (e.g., blue to black), or from reflecting any color combination to any other color (e.g., white to red). Referring to Fig. 3, in a specific pixel design, substrate 402 is glass, matching layer 404 is a film of zirconium dioxide which is 54.46 nm thick, absorber 406 is a tungsten film 14.49 nm thick, matching layer 408 is a film of silicon dioxide 50 nm thick, spacer 400 is air, and reflector 410 is a film of silver at least 50 nm thick. Referring to Fig. 4, the optical response of the pixel is shown in the driven state, i.e., when reflector 410 is in contact with matching layer 408 resulting in a broad state of induced absorption. Referring to Figs. 5-8, the different color pixels are shown in respective undriven states which correspond to the reflection of blue, green, red, and white light, respectively. These responses correspond to undriven spacer thicknesses of 325, 435, 230, and 700 nm respectively

Referring to Fig. 9, a section of a full color reflective flat panel display 298 includes three kinds of pixels, R, G, and B. Each kind differs from the others only in the size of the undriven spacer which is determined during manufacture as described in the parent patent application. Induced absorber 300 resides on substrate 304, and reflector 308 is self-supporting. Monolithic backplate 302 provides a hermetic seal and can consist a thick organic or inorganic film. Alternatively, the backplate may consist of a separate piece, such as glass, which has been aligned and bonded to the substrate. Electrodes may reside on this backplate so that the electromechanical performance of

the pixels may be modified. Incident light 310 is transmitted through optical compensation mechanism 306 and substrate 304 where it is selectively reflected or absorbed by a pixel. The display may be controlled and driven by circuitry of the kind described in the parent application.

Optical compensation mechanism 306 serves two functions in this display . The first is that of mitigating or eliminating the shift in reflected color with respect to the angle of incidence. This is a characteristic of all interference films and can be compensated for by using films with specifically tailored refractive indices or holographic properties, as well as films containing micro-optics; other ways may also be possible. The second function is to supply a supplemental frontlighting source. In this way, additional light can be added to the front of the display when ambient lighting conditions have significantly diminished thus allowing the display to perform in conditions ranging from intense brightness to total darkness. Such a frontlight could be fabricated using patterned organic emitters or edge lighting source coupled to a micro-optic array within the optical compensation film; other ways may also be possible.

The general process for fabrication of the devices is set forth in the parent application. Additional details of two alternative ways to fabricate spacers with different sizes are as follows; other ways may also be possible.

Both alternative processes involve the iterative deposition and patterning of a sacrificial spacer material which, in the final step of the larger process is, etched away to form an air-gap.

Referring to Fig. 10a, substrate 1000 is shown with induced absorber 1002 already deposited and

photoresist 1004 deposited and patterned. Induced absorber 1002 is deposited using any number of techniques for thin film deposition including sputtering and e-beam deposition. The photoresist is deposited via spinning, and patterned by overexposure to produce a natural overhang resulting in a stencil. The result is that it may be used to pattern subsequently deposited materials using a procedure known as lift-off. Referring to Fig. 10b, spacer material 1006 has been deposited, resulting in excess spacer material 1008 on top of the stencil. Referring to Fig. 10c, the stencil along with the excess spacer material have been lifted off by immersing the device in a bath of a solvent such as acetone and agitating it with ultrasound. Referring to Fig. 10d, the process has begun again with new photoresist 1010 having been deposited patterned in a fashion such that new spacer 1012 is deposited adjacent to the old spacer 1006. Repeating the process once more results in spacers with three different thicknesses. Referring to Fig. 10d, the process has begun again with new photoresist 1010 having been deposited patterned in a fashion such that new spacer 1012, with a different thickness, is deposited adjacent to the old spacer 1006.

Referring to Fig. 11a, substrate 1000 is shown with induced absorber 1102 already deposited. Spacer materials 1104, 1106, and 1108 have also been deposited and patterned by virtue of lift-off stencil 1110. The spacer materials have a thickness corresponding to the maximum of the three thicknesses required for the pixels. Referring to Fig. 11b, the stencil along with the excess material has been lifted off and new photoresist 1112 has been deposited and patterned such that spacer 1104 has been left exposed. Referring to Fig. 11c, spacer material 1104 has been etched back via one of a number of techniques which include wet chemical etching, and

reactive ion etching. Only a portion of the required spacer material is etched away, with the remainder to be etched in a subsequent etch step. Photoresist 1112 is subsequently removed using a similar technique. Referring to Fig. 11d, new photoresist 1114 has been deposited and patterned exposing spacers 1104 and 1106. The entire etch of spacer 1106 is performed in this step, and the etch of spacer 1104 is completed. Photoresist 1114 is subsequently removed and the process is complete.

Other embodiments are within the scope of the following claims.

For example, the spacer material need not ultimately be etched away but may remain instead a part of the finished device. In this fashion, and using the previously described patterning techniques, arbitrary patterns may be fabricated instead of arrays of simple pixels. Full color static graphical images may thus be rendered in a method which is analogous to a conventional printing process. In conventional printing, an image is broken up into color separations which are basically monochrome graphical subsets of the image, which correspond to the different colors represented, i.e., a red separation, a blue separation, a green separation, and a black separation. The full-color image is produced by printing each separation using a different colored ink on the same area.

Alternatively, in a process which we will call "Iridescent Printing", the different separations are composed of layers of thin films which correspond to the IMod design described here and those in the referenced patent. Patterning or printing a combination of colors or separations on the same area, allows for brilliant full-color images to be produced.

Referring to Fig. 12a, a square substrate is shown with area 1200 representing the portion of the substrate

which has been patterned with a thin film stack optimized for black. Referring to Fig. 12b, the substrate has been subsequently patterned with a thin film stack optimized for red in area 1202. Referring to Fig. 12c, the substrate has been subsequently patterned with a thin film stack optimized for green in area 1204. Referring to Fig. 12d, the substrate has been subsequently patterned with a thin film stack optimized for blue in area 1206.

Alternatively, a simpler process can be obtained if only the induced absorber design is used. In this process, the entire substrate is first coated with the induced absorber stack. Subsequent steps are then used to pattern the spacer material only, using the aforementioned techniques. After the desired spacers, i.e., colors are defined, a final deposition of a reflector is performed.

The brightness of different colors can be altered by varying the amount of black interspersed with the particular color i.e. spatial dithering. The images also exhibit the pleasing shift of color with respect to viewing angle known as iridescence.

In another example, a reflective flat panel display may also be fabricated using a single kind of pixel instead of three. Multiple colors, in this case, are obtained through fabricating the pixels in the form of continuously tunable or analog interferometric modulators as described in the parent patent application. In this fashion, any individual pixel may, by the application of the appropriate voltage, be tuned to reflect any specific color. This would require that the array be fabricated on a substrate along with electronic circuitry, or directly on the surface of an integrated circuit, in order to provide a charge storage mechanism. This approach, though it requires a more complicated

driving scheme relying on analog voltages, provides superior resolution. It would also find application in a projection system.

What is claimed is:

Claims

1. An interferometric modulator cavity comprising
a reflector, and
an induced absorber.

2. The cavity of claim 1 wherein the reflector comprises a film of metal.

3. The cavity of claim 1 wherein the reflector comprises a dielectric film.

4. The cavity of claim 1 wherein the reflector comprises a semiconducting film.

5. The cavity of claim 1 wherein the reflector comprises a combination of at least two of a metal film, a dielectric film, and a semiconducting film.

6. The cavity of claim 1 wherein the induced absorber comprises a sandwich of an absorber between two matching layers.

7. The cavity of claim 6 wherein one of the matching layers resides at the boundary of the absorber with an incident medium and the other matching layer resides at the boundary of the absorber with the reflector.

8. The cavity of claim 6 wherein at least one of the matching layers comprises a film of metal.

9. The cavity of claim 6 wherein at least one of the matching layers comprises a dielectric film.

10. The cavity of claim 6 wherein at least one of the matching layers comprises a semiconducting film.

11. The cavity of claim 6 wherein at least one of the matching layers comprises a combination of at least two of a metal film, a dielectric film, and a semiconducting film.

12. The cavity of claim 6 wherein the absorber comprises a high loss film such as a metal.

13. The cavity of claim 6 wherein the absorber comprises a high loss film such as a semiconductor.

14. The cavity of claim 6 wherein the absorber comprises a combination of a metal and semiconducting film.

15. The cavity of claim 1 further comprising a substrate.

16. The cavity of claim 15 wherein the substrate comprises a transparent incident medium.

17. The cavity of claim 15 wherein the substrate comprises an opaque medium.

18. The cavity of claim 15 wherein the induced absorber resides on the substrate.

19. The cavity of claim 15 wherein the reflector resides on the substrate.

20. The cavity of claim 15 wherein the substrate comprises a reflector.

21. A direct view reflective flat panel display comprising an array of interferometric modulators.

22. The display of claim 21 wherein the array comprises a single set of analog interferometric modulators arranged to tune to any specific reflective state.

23. The display of claim 21 wherein the array comprises sets of the interferometric modulators the respective sets being arranged to switch between different pairs of reflective states.

24. The display of claim 21 wherein the brightness of each of the modulators is controlled by pulse width modulation.

25. The display of claim 21 wherein the brightness of each of the modulators pixel is controlled by spatial dithering.

26. The display of claim 21 wherein the brightness of each of the modulators is controlled by a combination of pulse width modulation and spatial dithering.

27. The display of claim 21 wherein the array is sealed by a backplane.

28. The display of claim 21 where the backplane comprises a monolithic element.

29. The display of claim 27 wherein the backplane is attached.

30. The display of claim 27 wherein the backplane supports electrodes which modify the electromechanical response of the pixels.

31. The display of claim 21 where each of the modulators is actuated by electrostatic forces.

32. The display of claim 21 wherein each of the modulators is actuated by piezoelectric forces.

33. The display of claim 21 wherein each of the modulators is actuated by magnetic forces.

34. The display of claim 21 for use in a projection system.

35. The display of claim 21 wherein an optical compensation mechanism is used to mitigate or eliminate a shift in color with respect to viewing angle, and to .

36. The display of claim 21 wherein an optical compensation mechanism is used to provide supplemental frontlighting.

37. The display of claim 21 wherein an optical compensation mechanism is used to mitigate or eliminate a shift in color with respect to viewing angle, and to provide supplemental frontlighting.

38. The display of claim 21 wherein the array is fabricated on an integrated circuit.

39. The display of claim 21 wherein the array is fabricated on the substrate along with electronic circuitry.

40. A process for fabricating adjacent spacers of different thicknesses on a substrate comprising

using a lift-off technique to pattern the spacers which are deposited separately, each deposition providing a different thickness of spacer.

41. A process for fabricating adjacent spacers of different thicknesses on a substrate comprising

using a patterned photoresist to allow for an etching process to selectively etch back the thickness of a spacer which was deposited in a single deposition.

42. A full-color static graphical image comprising

an array of interferometric modulator cavities, each cavity including

a reflector, and

an induced absorber, the induced absorber including a spacer having a thickness that defines a color associated with the cavity.

43. The image of claim 42 in which the brightness of different cavities is determined by spatial dithering.

44. A full-color static graphical image comprising separate patterns of spacers or of interferometric modulator cavities and spacers, for each pattern the spacer having a thickness which defines a color associated with the pattern which when all patterns are combined produces the image.

45. The image of claim 44 in which the brightness of the different patterns is determined by spatial dithering.

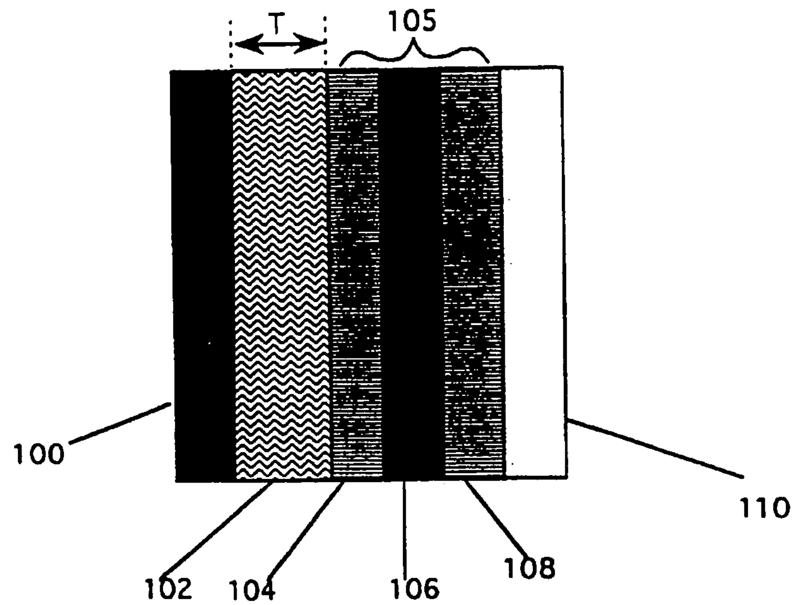


FIG. 1

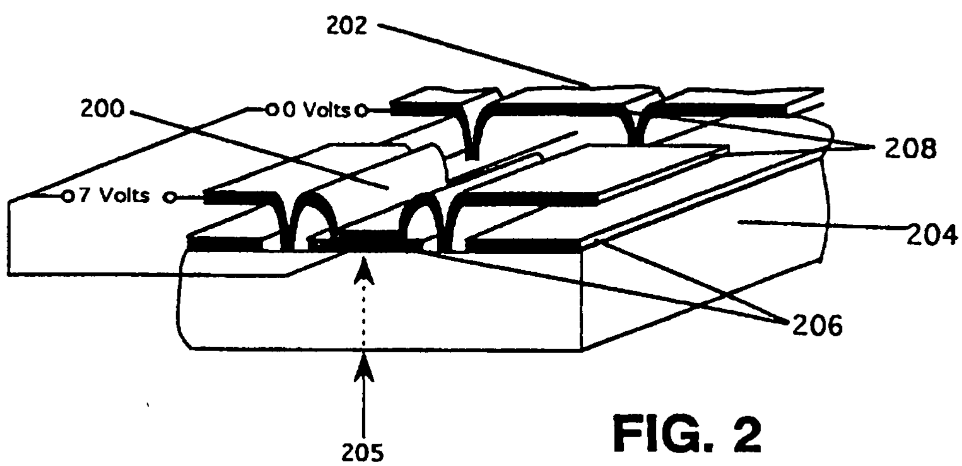


FIG. 2

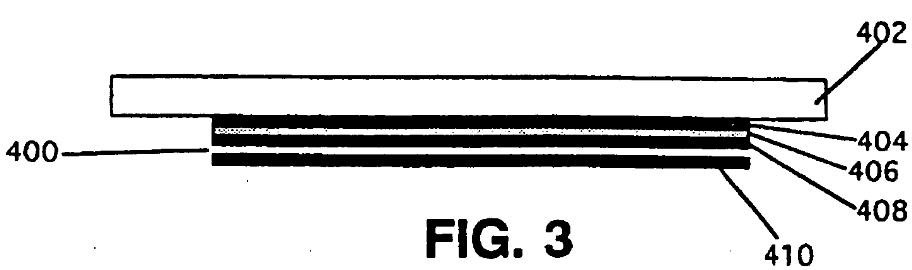
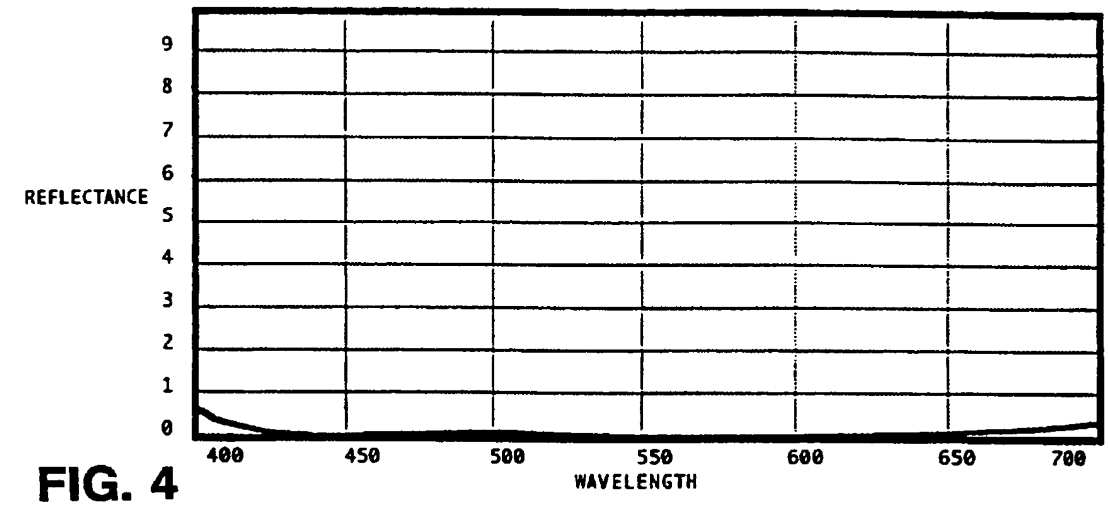
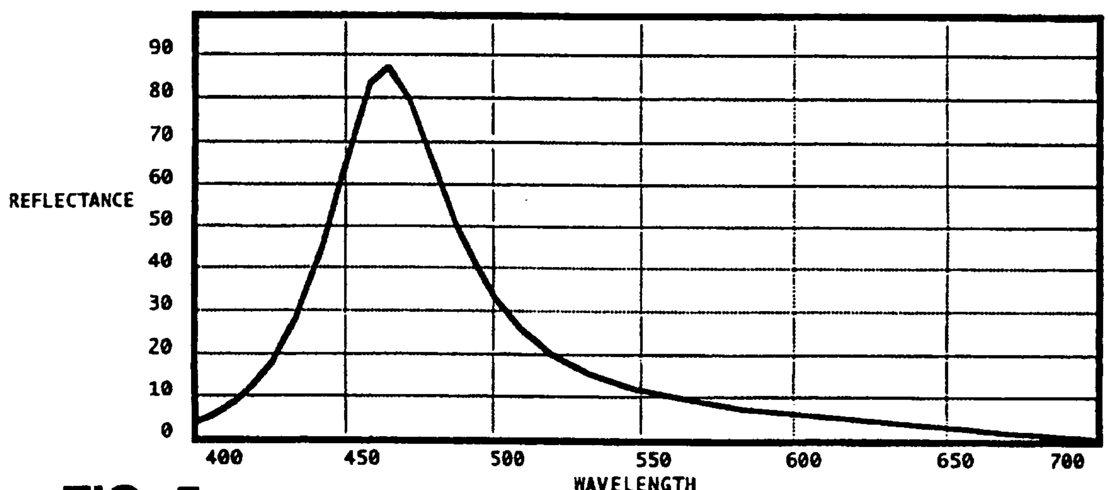


FIG. 3



REFLECTANCE
9
8
7
6
5
4
3
2
1
0
400
450
500
550
600
650
700
WAVELENGTH

FIG. 4



REFLECTANCE
90
80
70
60
50
40
30
20
10
0
400
450
500
550
600
650
700
WAVELENGTH

FIG. 5

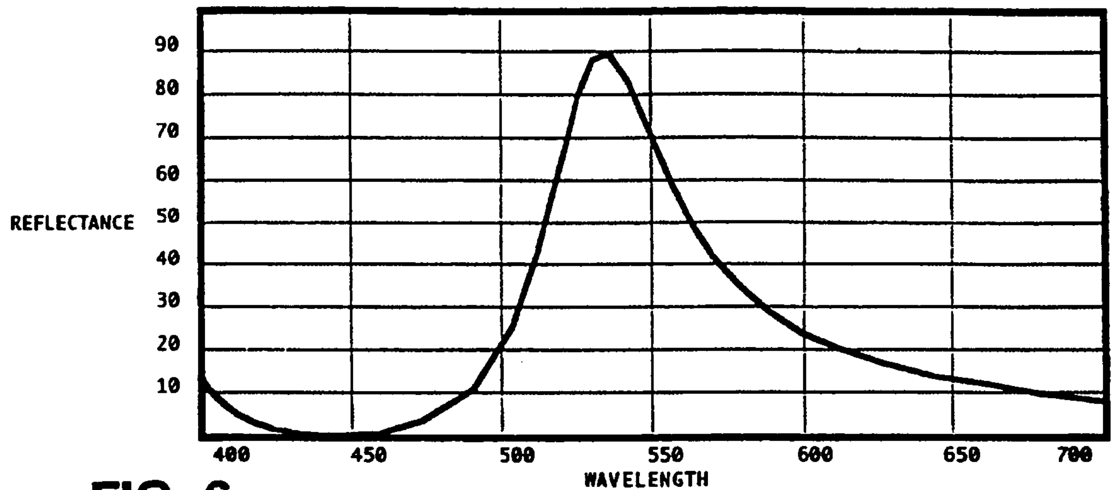

REFLECTANCE
90
80
70
60
50
40
30
20
10
400
450
500
550
600
650
700
WAVELENGTH

FIG. 6

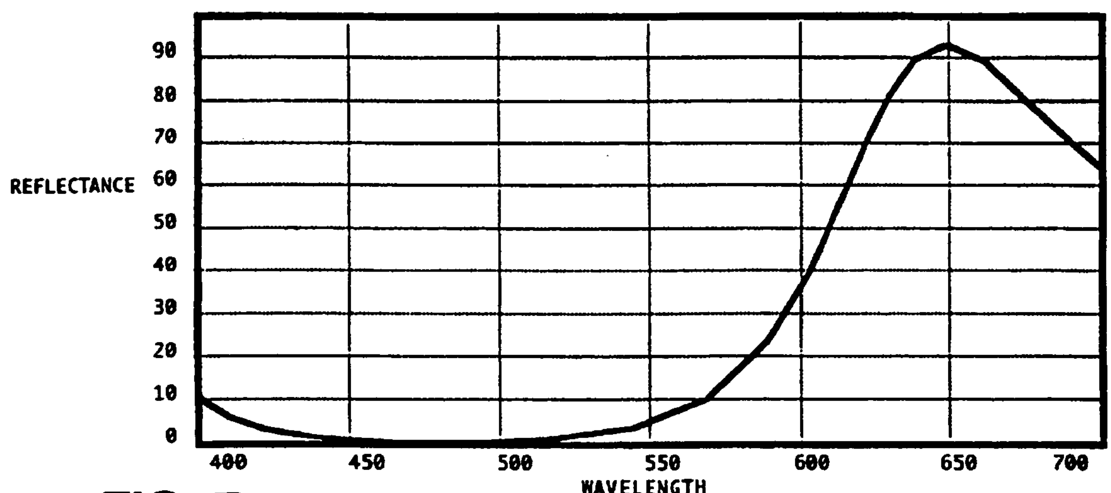

REFLECTANCE
90
80
70
60
50
40
30
20
10
0
400
450
500
550
600
650
700
WAVELENGTH

FIG. 7

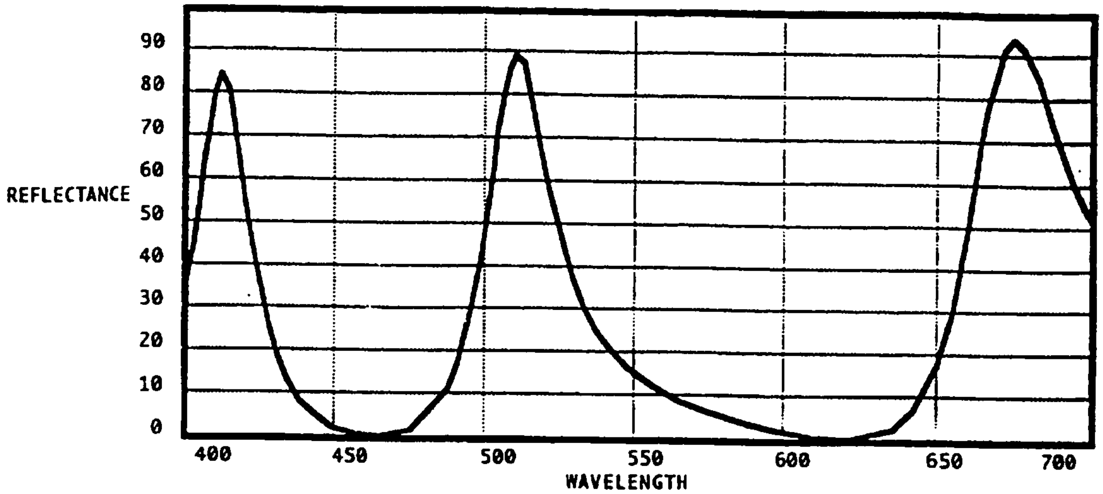

90
80
70
60
REFLECTANCE
50
40
30
20
10
0
400
450
500
550
600
650
700
WAVELENGTH

FIG. 8

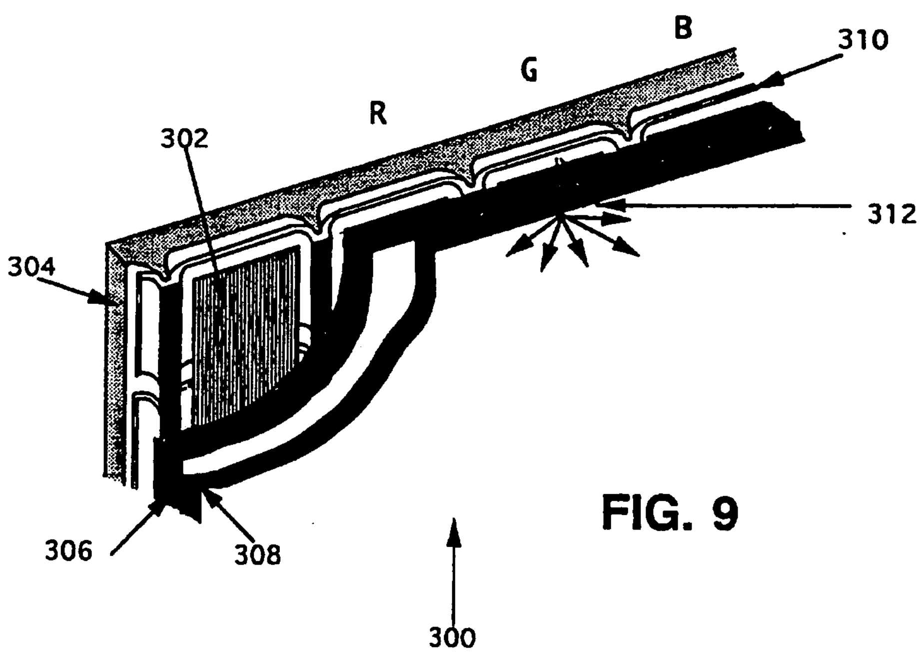

B
G
R
310
302
312
304
306
308
300

FIG. 9

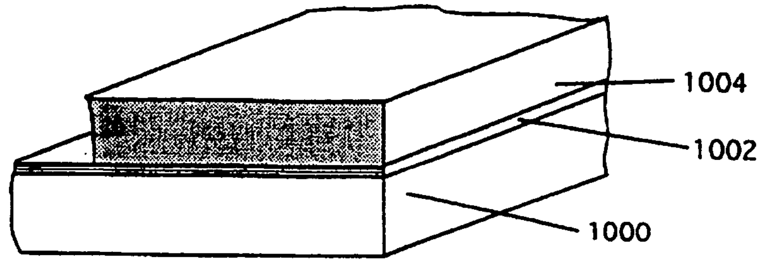


FIG. 10a

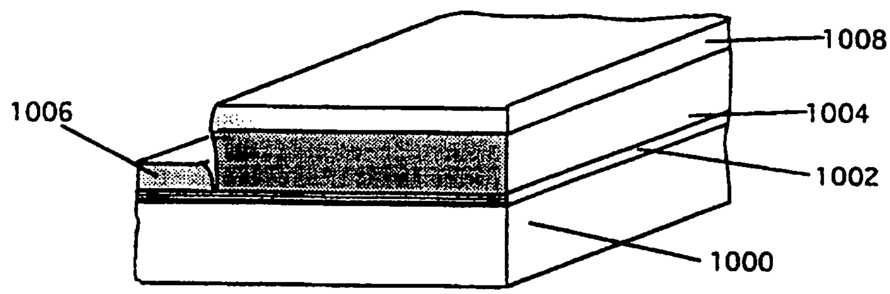


FIG. 10b

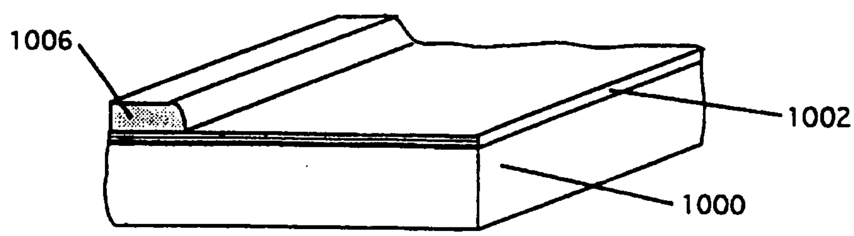


FIG. 10c

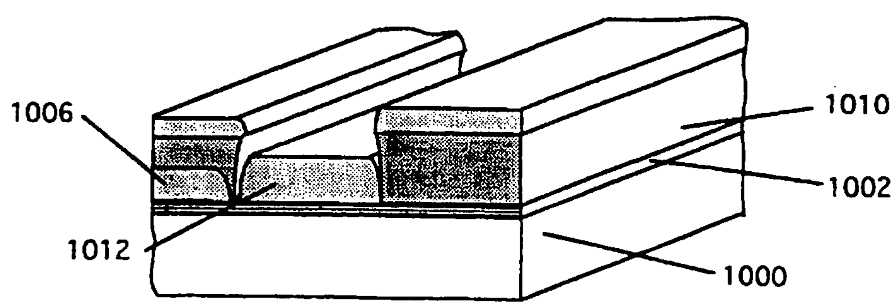


FIG. 10d

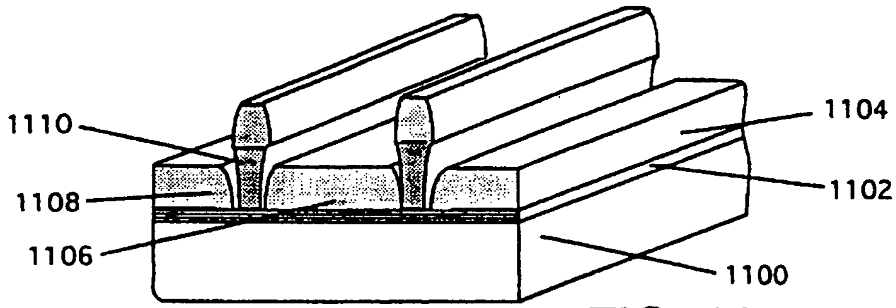


FIG. 11a

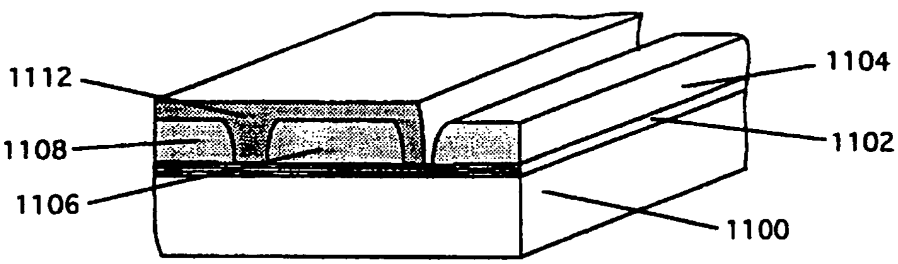


FIG. 11b

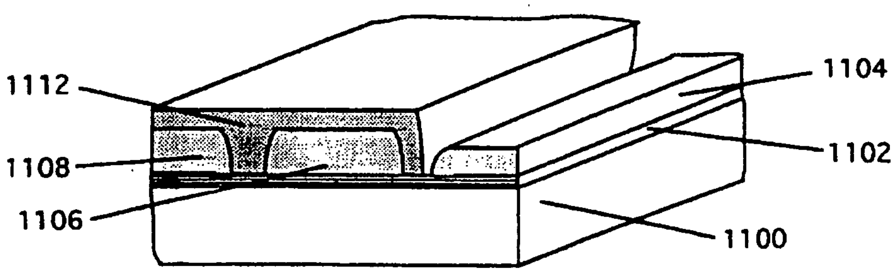


FIG. 11c

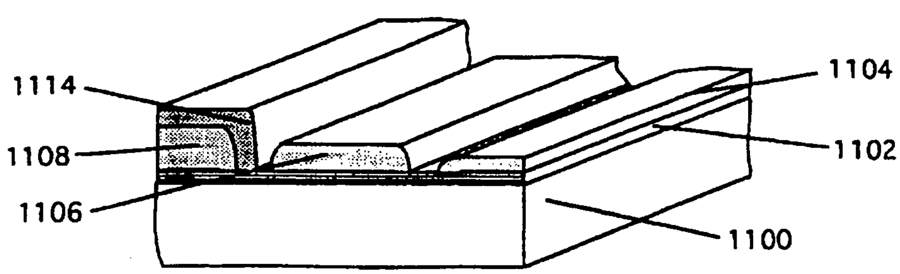


FIG. 11d

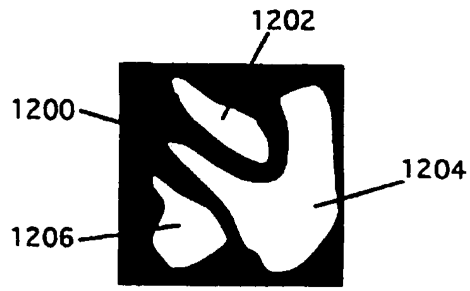


FIG. 12a

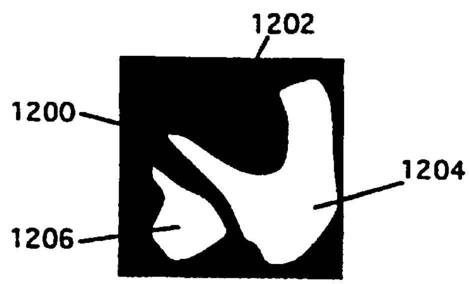


FIG. 12b

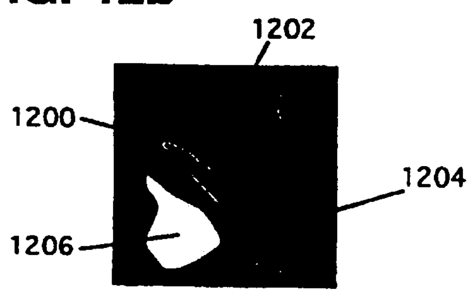


FIG. 12c

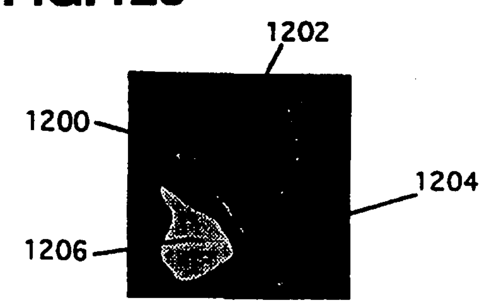


FIG. 12d

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US96/17731

A. CLASSIFICATION OF SUBJECT MATTER

IPC(6) :G02B 26/00
US CL :359/290, 291
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 359/290, 291, 295, 263, 580, 584, 585, 586, 589

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X,P ----- Y,P	US 5,500,761 A (GOSSEN ET AL) 19 March 1996 (19/03/96), see entire document.	1, 2, 15-17, 19, & 20 ---------- 3-14 & 18
Y	US 4,377,324 A (DURAND ET AL) 22 March 1983 (22/03/83), see entire document.	3-14 & 18

☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search
09 JANUARY 1997

Date of mailing of the international search report
28 JAN 1997

Name and mailing address of the ISA/US
Commissioner of Patents and Trademarks
Box PCT
Washington, D.C. 20231
Facsimile No. (703) 305-3230

Authorized officer
JOHN P. CORNELY
Telephone No. (703) 308-4836

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US96/17731

Box I Observations where certain claims were found unsearchable (Continuation of item 1 of first sheet)

This international report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. ☐ Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:

2. ☐ Claims Nos.:
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:

3. ☐ Claims Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box II Observations where unity of invention is lacking (Continuation of item 2 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

Please See Extra Sheet.

1. ☐ As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.

2. ☐ As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee.

3. ☐ As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:

4. ☒ No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:
1-20

Remark on Protest ☐ The additional search fees were accompanied by the applicant's protest.
☐ No protest accompanied the payment of additional search fees.

BOX II. OBSERVATIONS WHERE UNITY OF INVENTION WAS LACKING
This ISA found multiple inventions as follows:

This application contains the following inventions or groups of inventions which are not so linked as to form a single inventive concept under PCT Rule 13.1. In order for all inventions to be searched, the appropriate additional search fees must be paid.

Group I, claim(s)1-20, drawn to interferometric modulator cavity, classified in Class 359, subclass 290.
Group II, claim(s) 21-39 and 42-45, drawn to a display, classified in Class 345, subclass 85.
Group III, claim(s) 40-41, drawn to a process for fabricating spacers, classified in Class 427, subclass 259.

The inventions listed as Groups I, II, and III do not relate to a single inventive concept under PCT Rule 13.1 because, under PCT Rule 13.2, they lack the same or corresponding special technical features for the following reasons: the three claimed inventions are simply unrelated, that is to say, there are no limitations in the claims of Group II that would require the use of the modulator of Group I and/or the spacers of Group III; as to Group I there are no limitations therein that even recite spacers or a display; and there are no limitations in Group III that would indicate that the process for fabricating the spacers is designed to create spacers for either the invention of Group I or Group II.